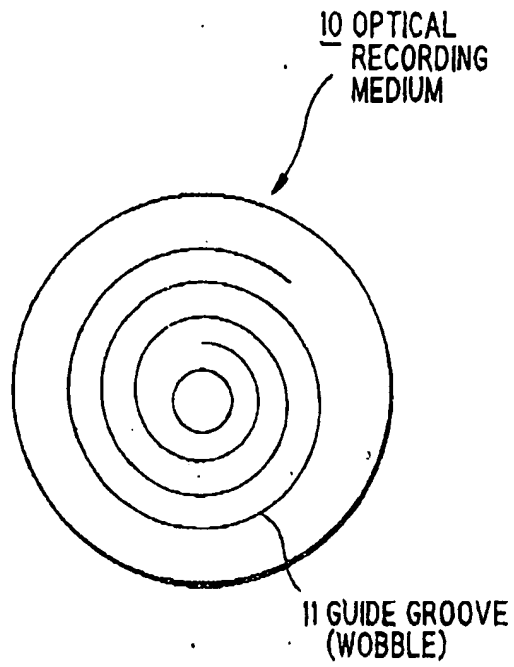


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FIG. 1



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FIG. 2(A)

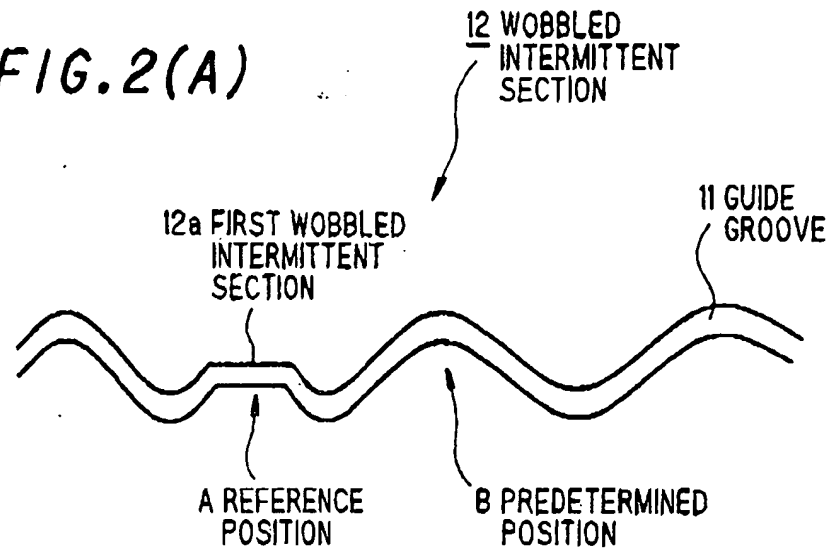
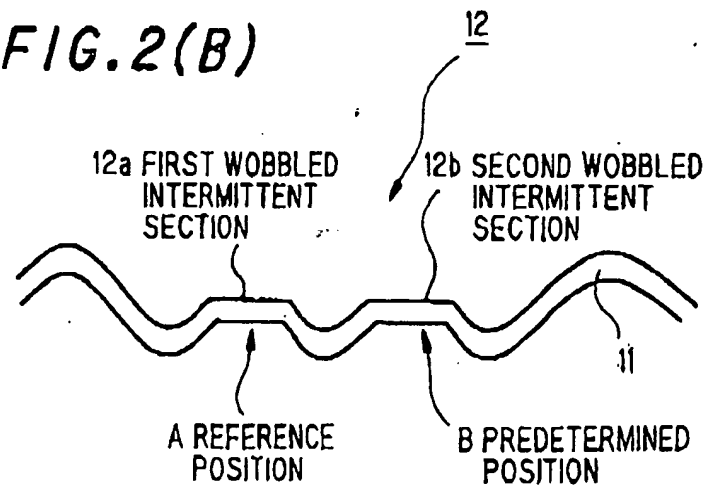
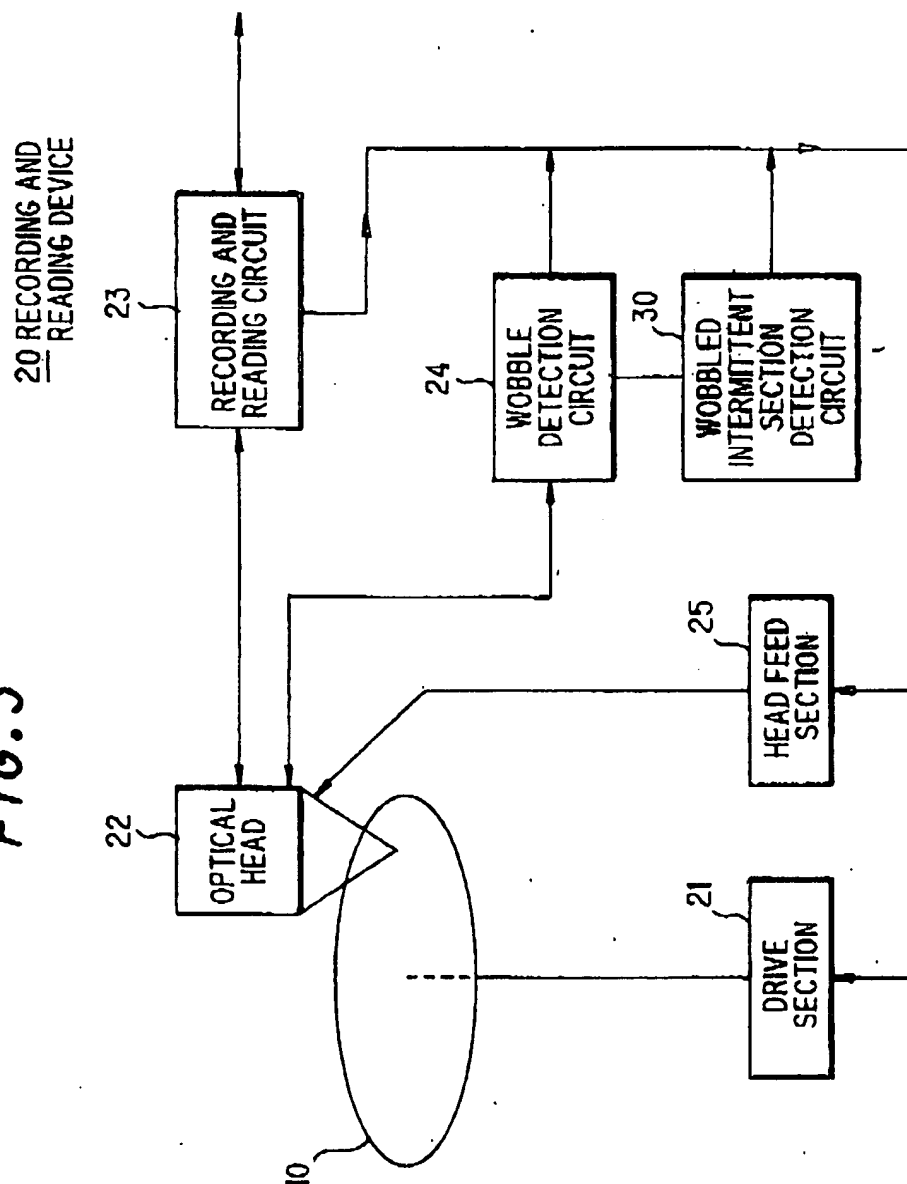


FIG. 2(B)



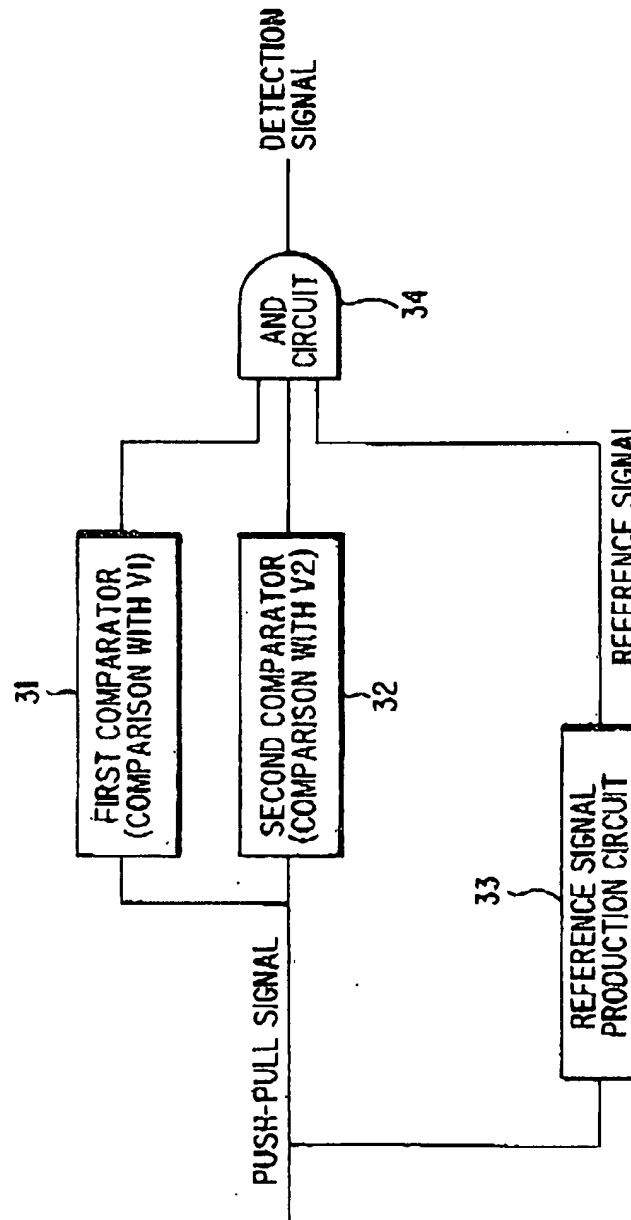
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FIG. 3



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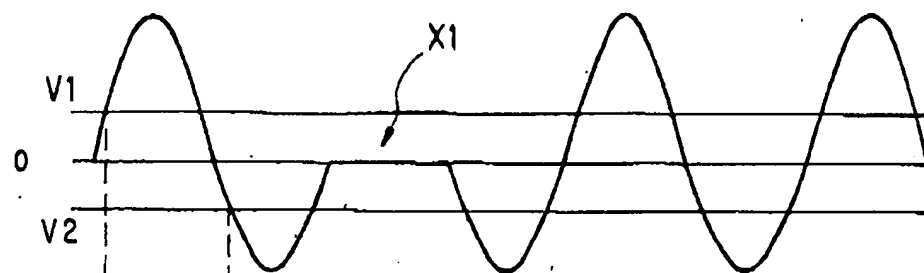
FIG. 4
30 WOBBLED INTERMITTENT
DETECTION CIRCUIT



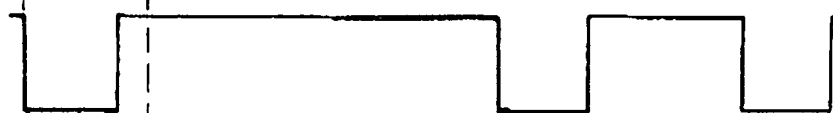
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FIG. 5

(A) PUSH-PULL SIGNAL



(B) OUTPUT OF COMPARATOR 31



(C) OUTPUT OF COMPARATOR 32



(D) REFERENCE SIGNAL



(E) DETECTION SIGNAL

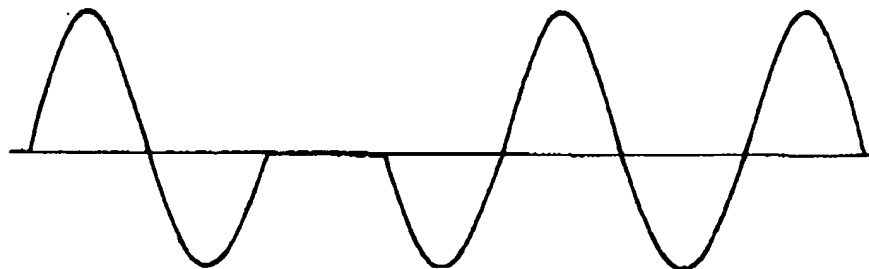


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FIG. 6

(A) LOGIC "0"

PUSH-PULL SIGNAL



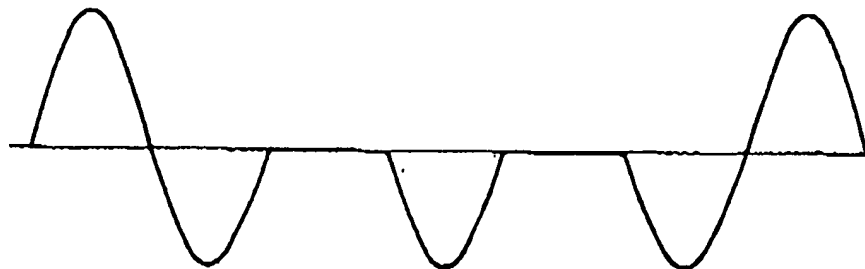
DETECTION SIGNAL

REFERENCE POSITION
SIGNALPREDETERMINED
REGION

ONE SET

(B) LOGIC "1"

PUSH-PULL SIGNAL

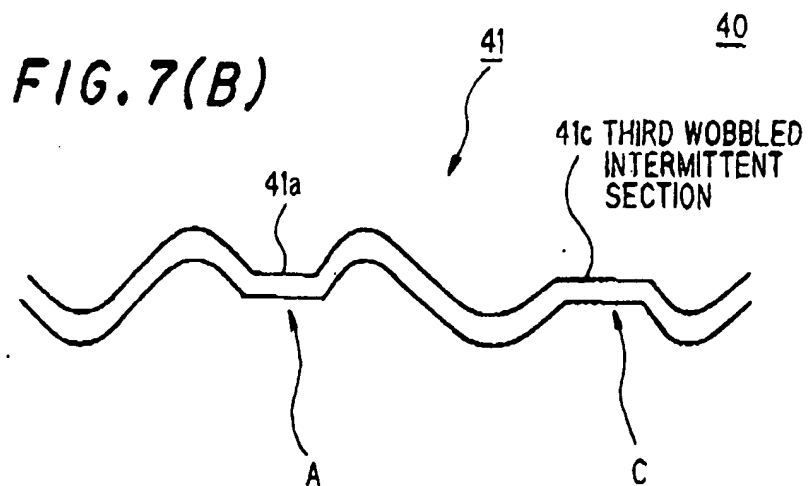
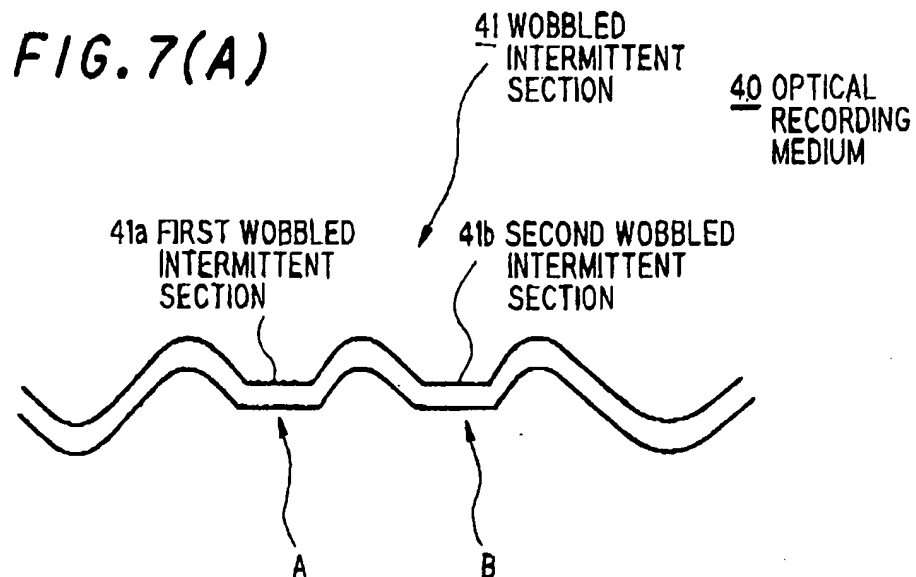


DETECTION SIGNAL

REFERENCE POSITION
SIGNALPREDETERMINED
REGION

ONE SET

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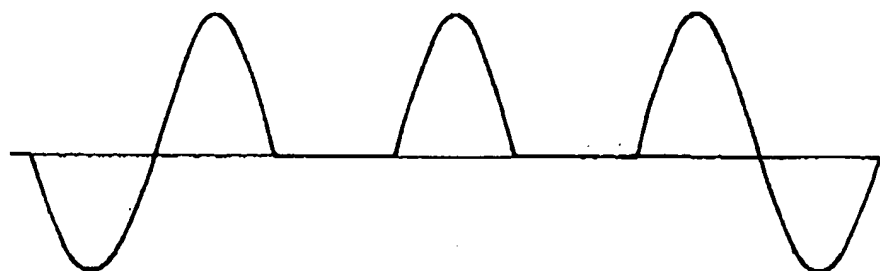


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FIG. 8

(A) **LOGIC "0"**

PUSH-PULL SIGNAL



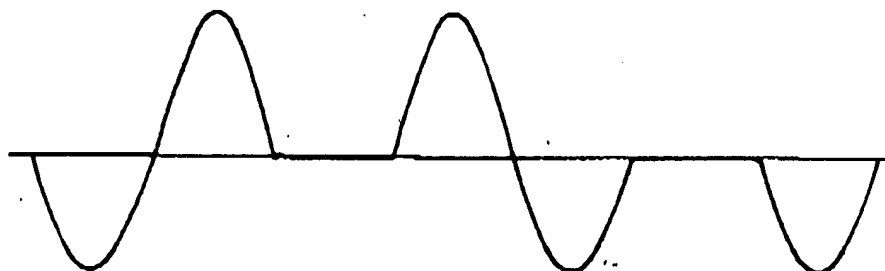
DETECTION SIGNAL

REFERENCE POSITION
SIGNAL

ONE SET

(B) **LOGIC "1"**

PUSH-PULL SIGNAL



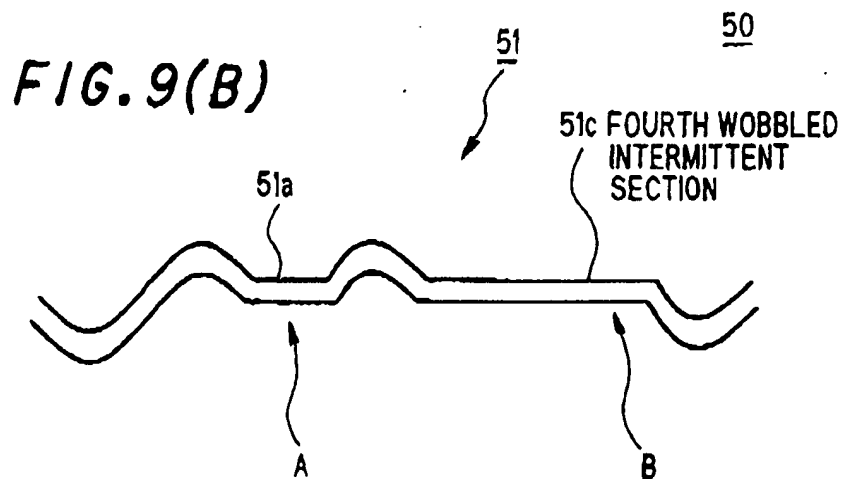
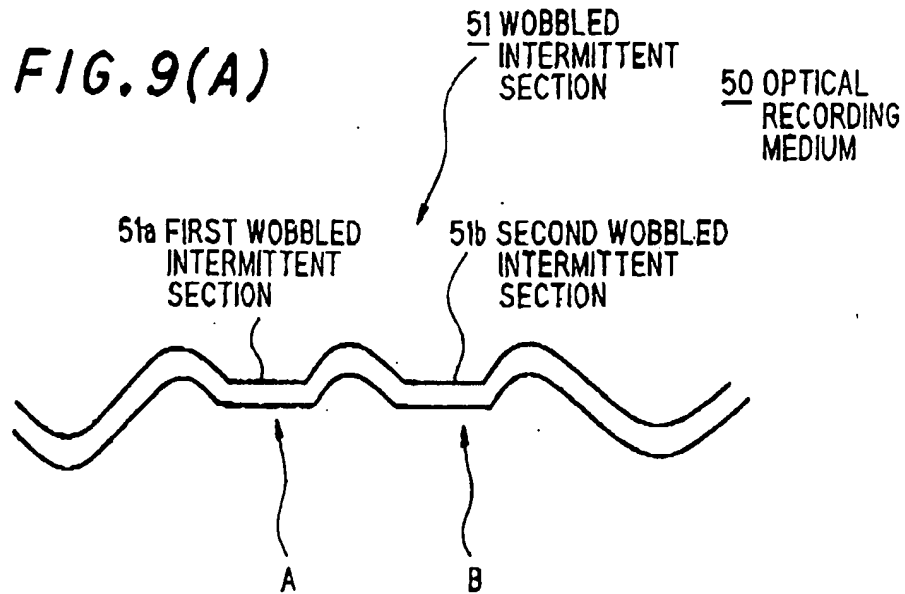
DETECTION SIGNAL

REFERENCE POSITION
SIGNAL

ONE SET

2002年 3月 4日 18時01分

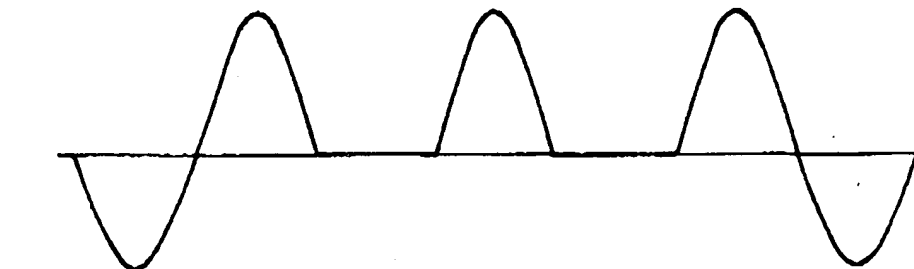
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FIG. 10(A) **LOGIC "0"**

PUSH-PULL SIGNAL



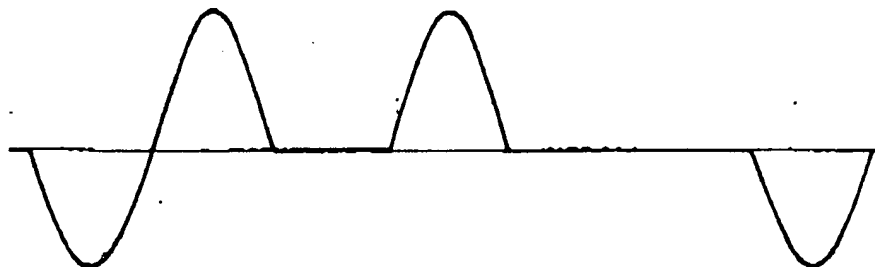
DETECTION SIGNAL

REFERENCE POSITION
SIGNAL

ONE SET

(B) **LOGIC "1"**

PUSH-PULL SIGNAL



DETECTION SIGNAL

REFERENCE POSITION
SIGNAL

ONE SET